

Figure 3. Block diagram of the target computer

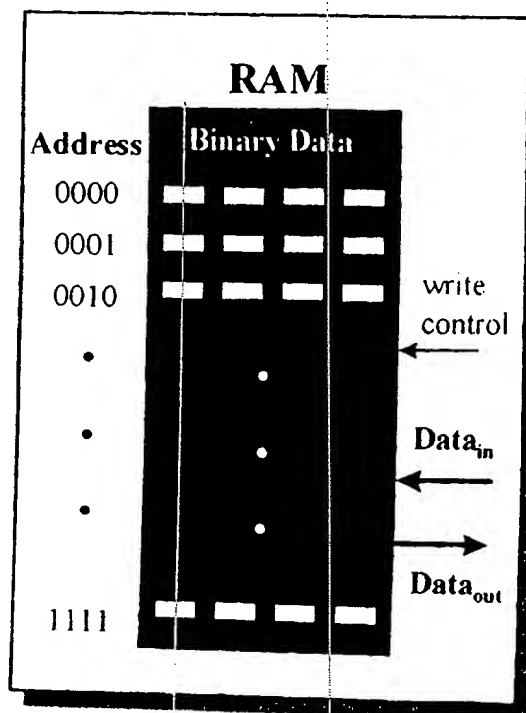


Fig 4